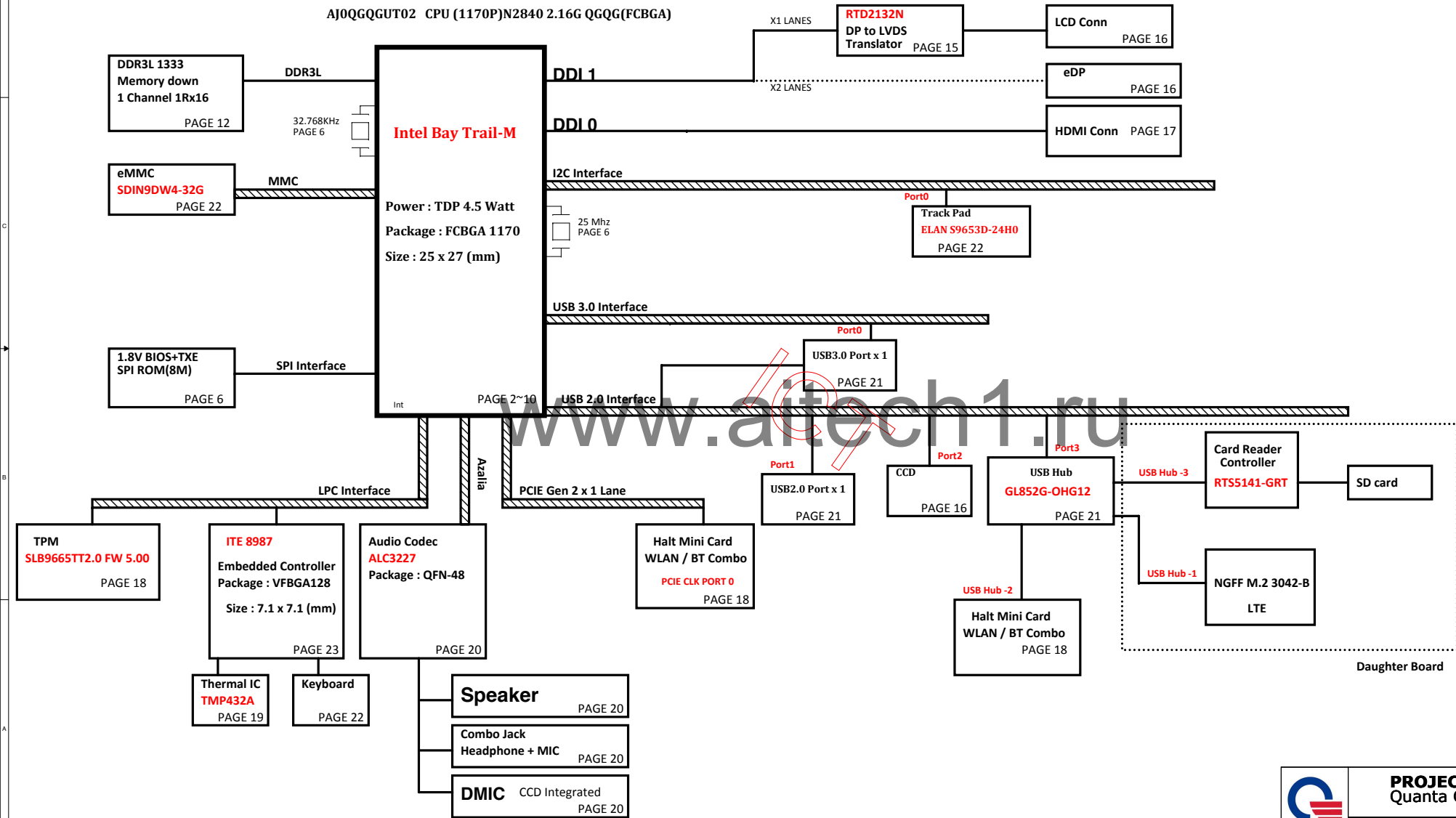


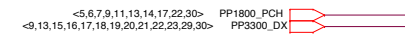
Y0A Intel Bay Trail-M Platform Block Diagram

AJ0QGQLUT02 CPU(1170P)N2940 1.83G QGQL(FCBGA)

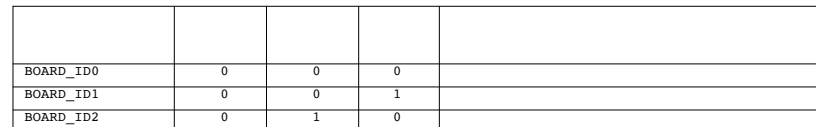
AJ0QGQGUT02 CPU (1170P)N2840 2.16G QGQG(FCBGA)



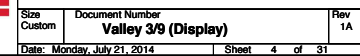


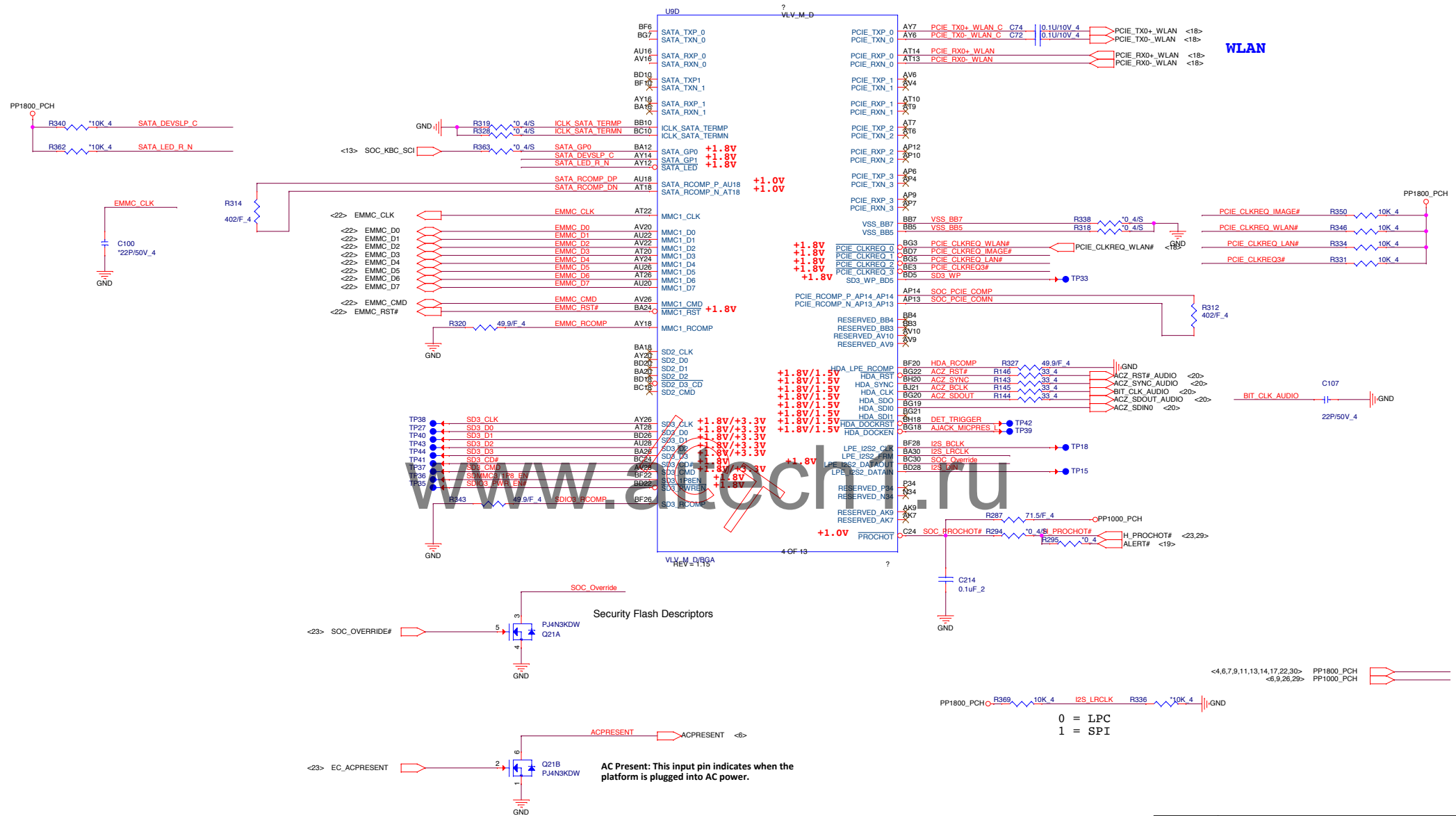


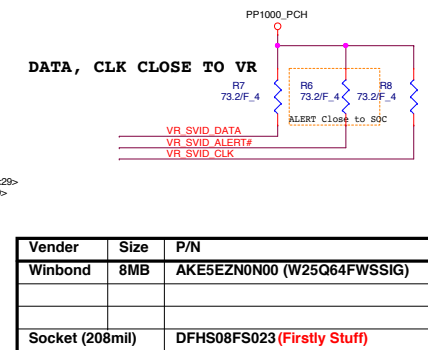
PP1800_PCH



BOARD_ID0	0	0	0	
BOARD_ID1	0	0	1	
BOARD_ID2	0	1	0	

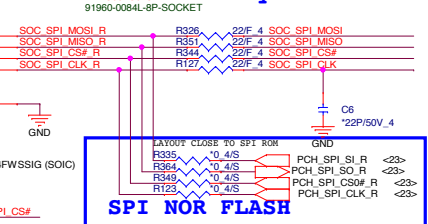





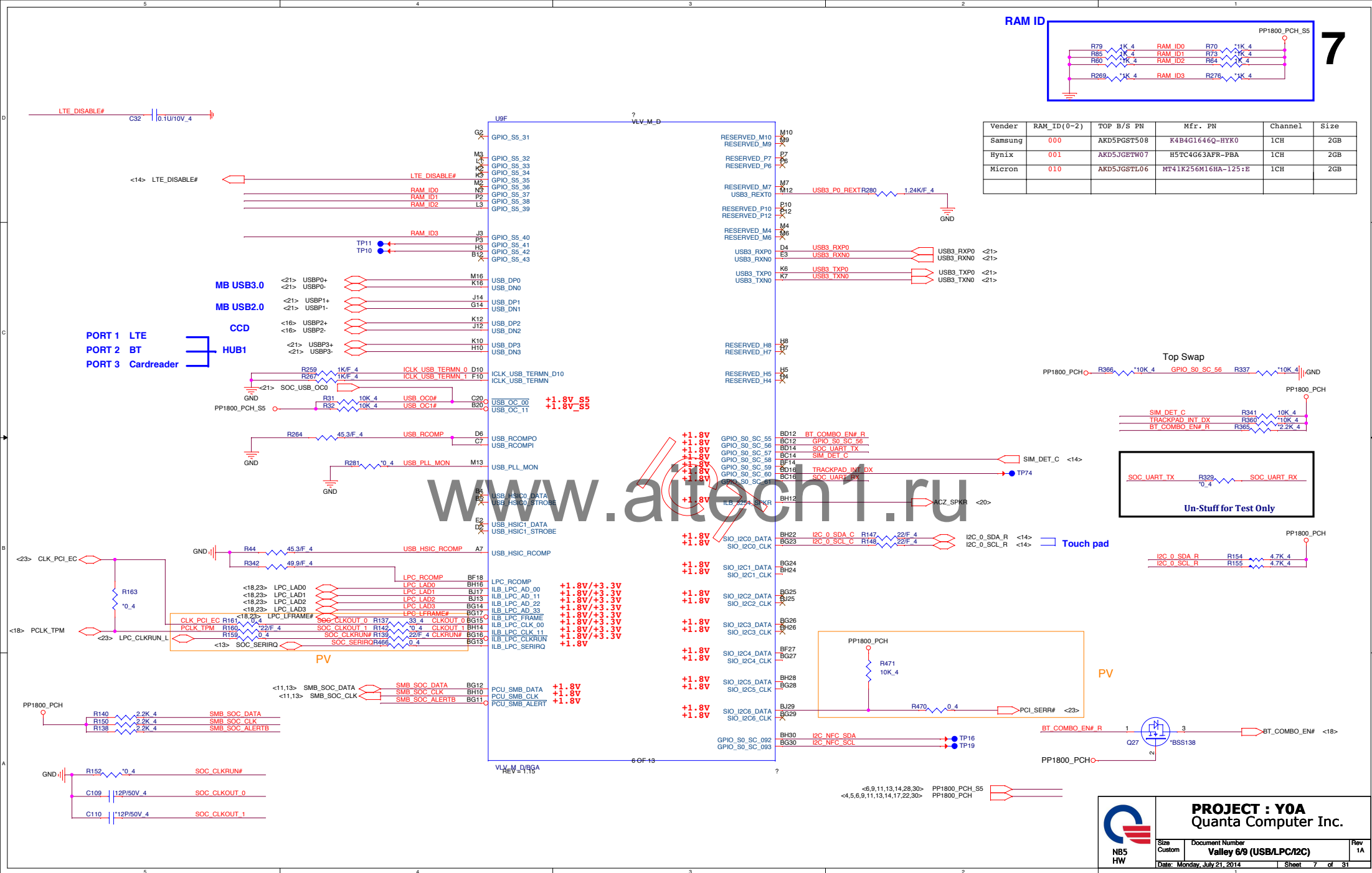


Vender	Size	P/N
Winbond	8MB	AKE5EZN0N00 (W25Q64FWSSIG)
Socket (208mil)		DFHS08FS023 (Firstly Stuff)

Co-layout with U2



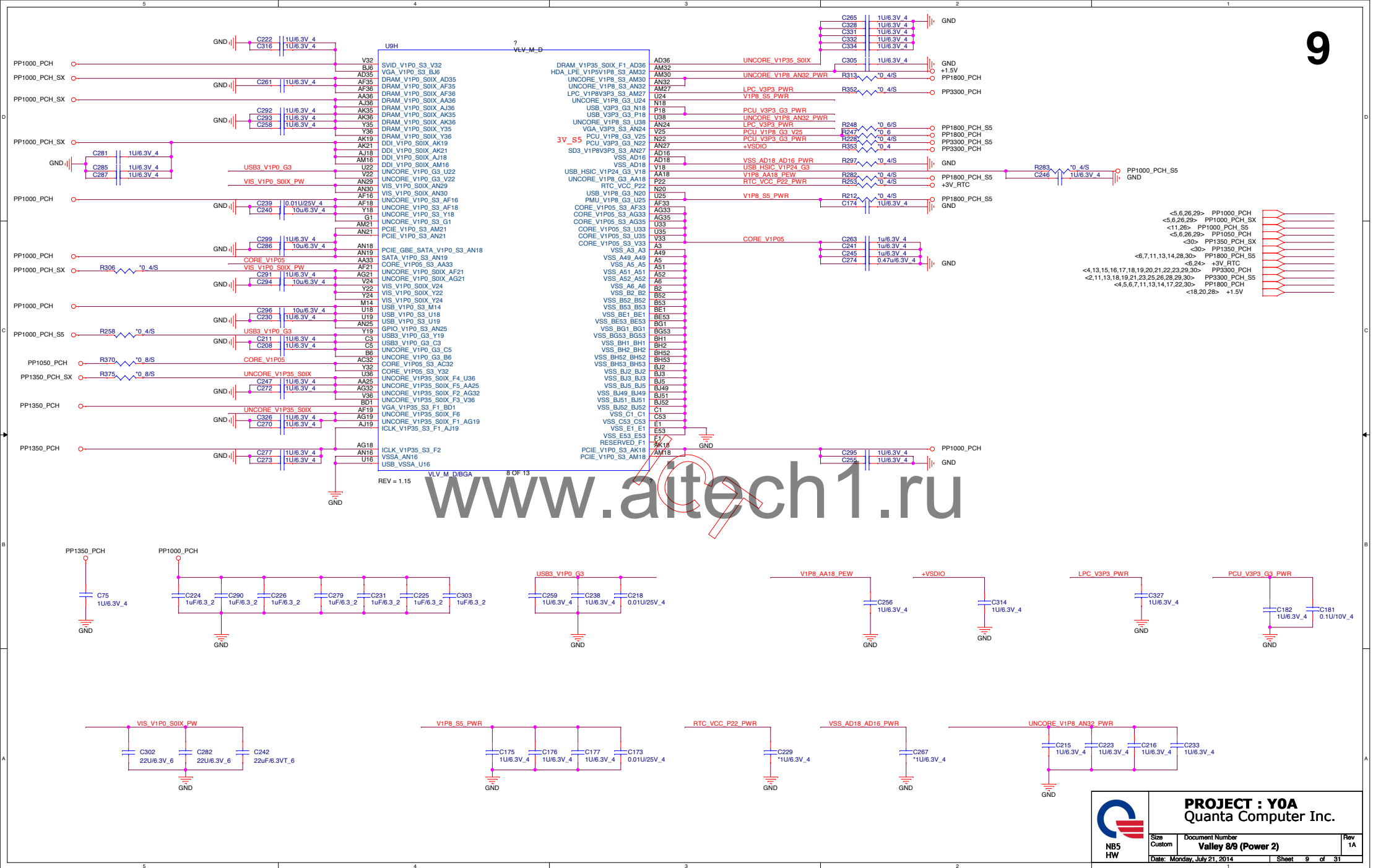
	PROJECT : Y0A Quanta Computer Inc.	
	Size Custom	Document Number Valley 5/9 (SPI/GPIO/CLK)
	Date: Monday, July 21, 2014	Sheet 6 of 6

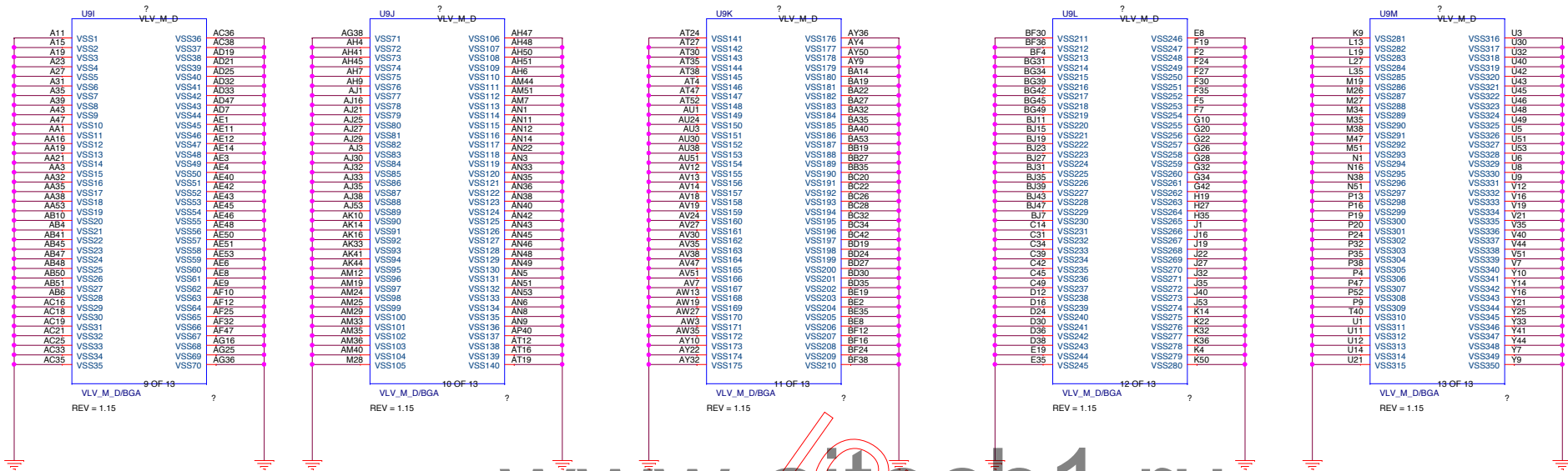


<29> VSS_AXG_SENSE 



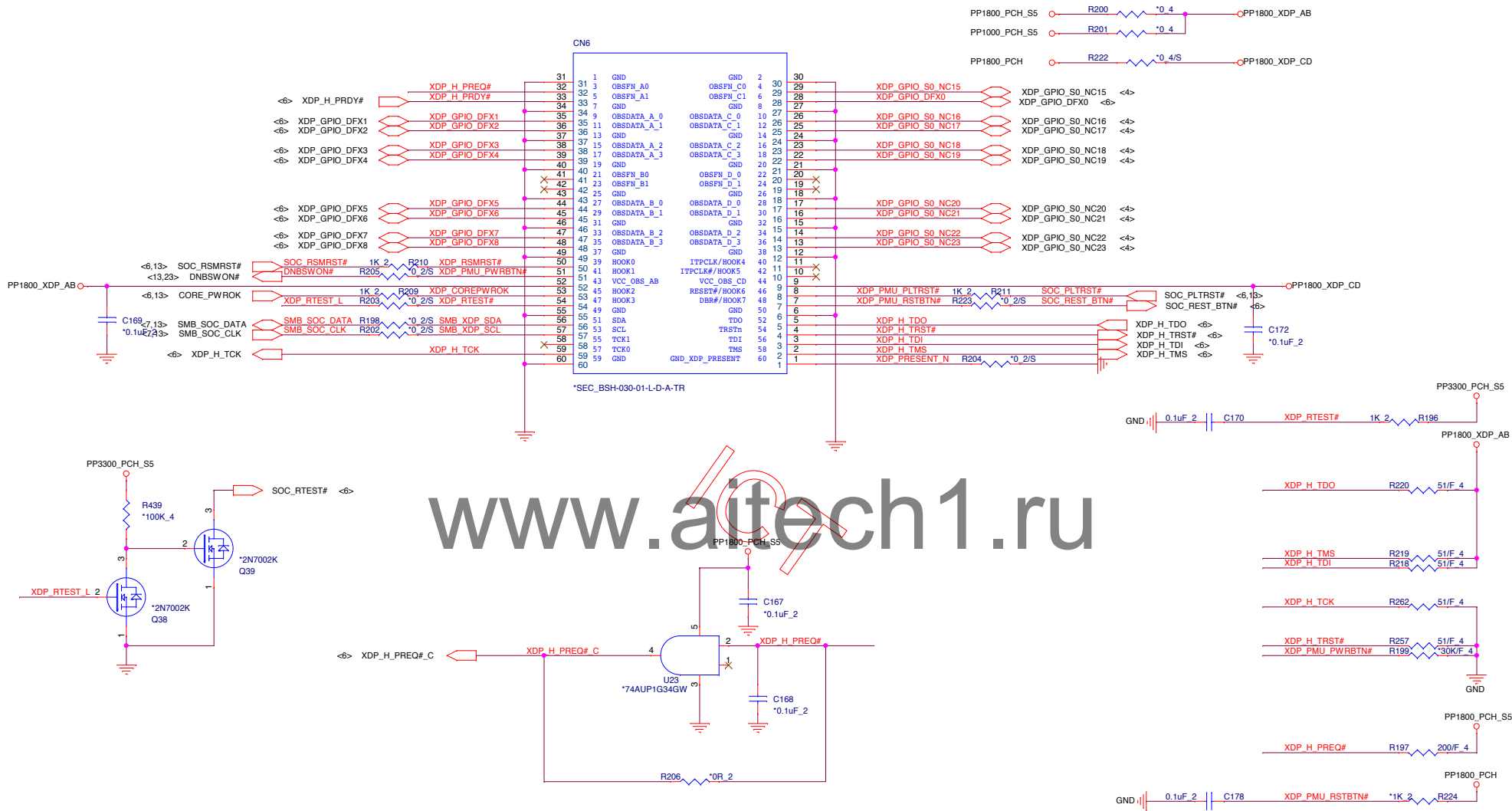
Size Custom	Document Number Valley 7/9 (Power 1)	Rev 1/
Date: Monday, July 21, 2014		Sheet 8 of 31






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Quanta Computer Inc.

Size	Document Number	Rev
Custom	Valley 9/9 (GND)	1A
Date: Monday, July 21, 2014	Sheet 10 of 31	



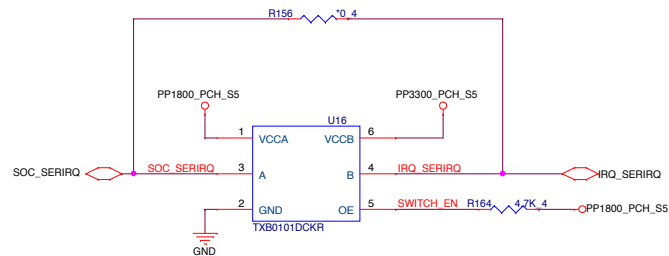
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<4,5,6,7,9,13,14,17,22,30>
<9,28>



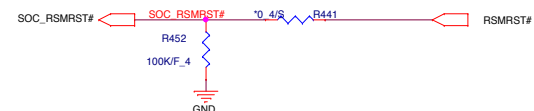
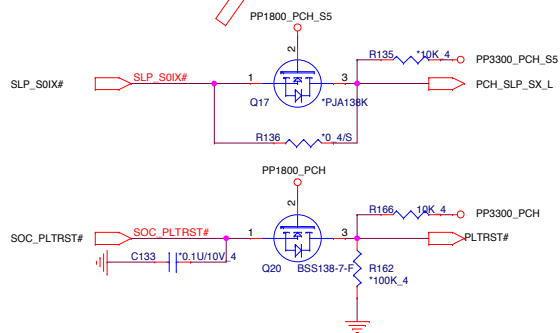
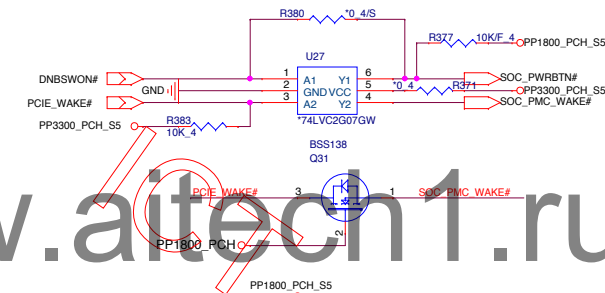
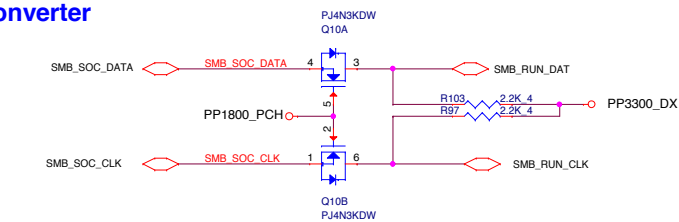
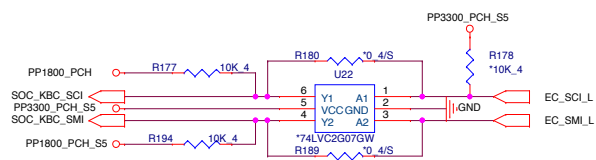
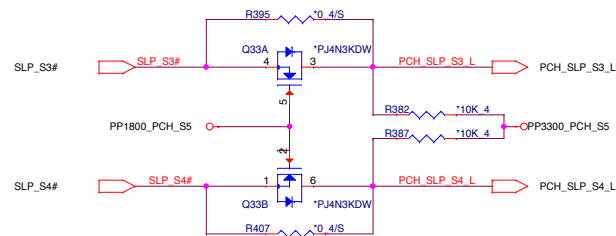
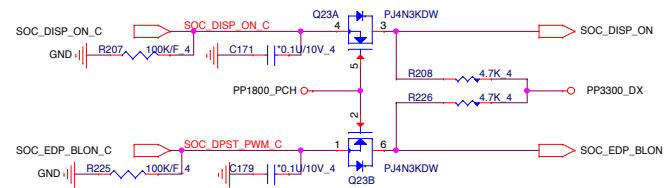
PROJECT : Y0A
Quanta Computer Inc.

Size Custom	Document Number CPU/PCH XDP	Rev 1A
Date: Monday, July 21, 2014		Sheet 11 of 31

eDP control pin (For eDP)



LVDS converter

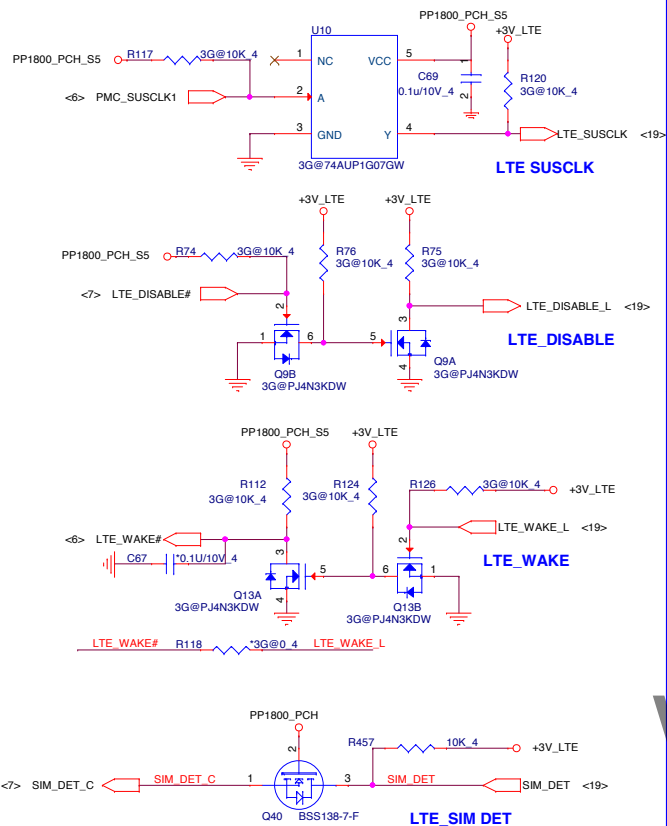


PP1800_PCH_S5
PP3300_PCH_S5
PP1800_PCH
PP3300_PCH
PP3300_DX

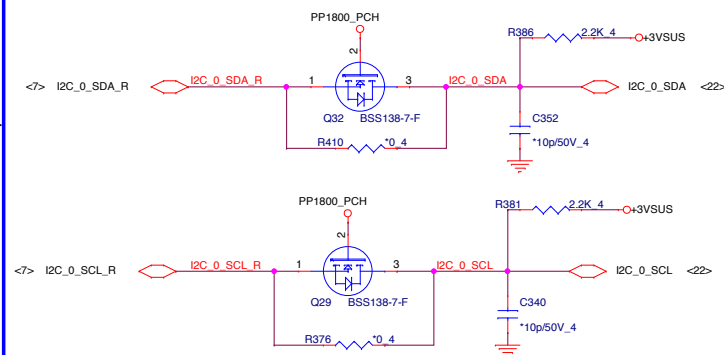


PROJECT : Y0A
Quanta Computer Inc.

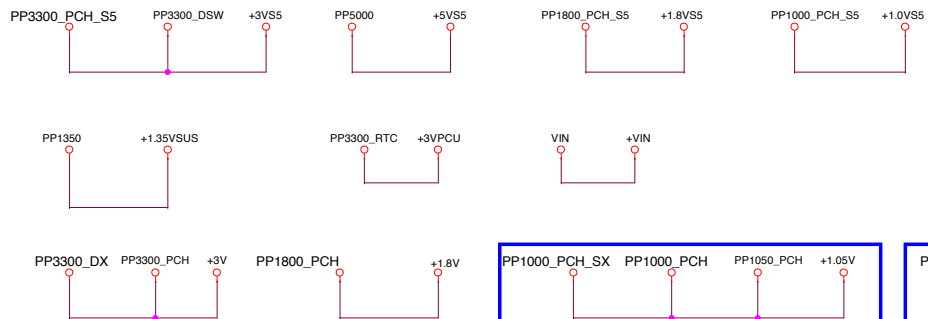
Size	Document Number	Rev
Custom	Level Shifter (SOC_EC)	1A
Date: Monday, July 21, 2014	Sheet 13 of 31	



Track Pad



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Combine +1.0VSX,+1.0V,+1.05V
power rail for cost saving

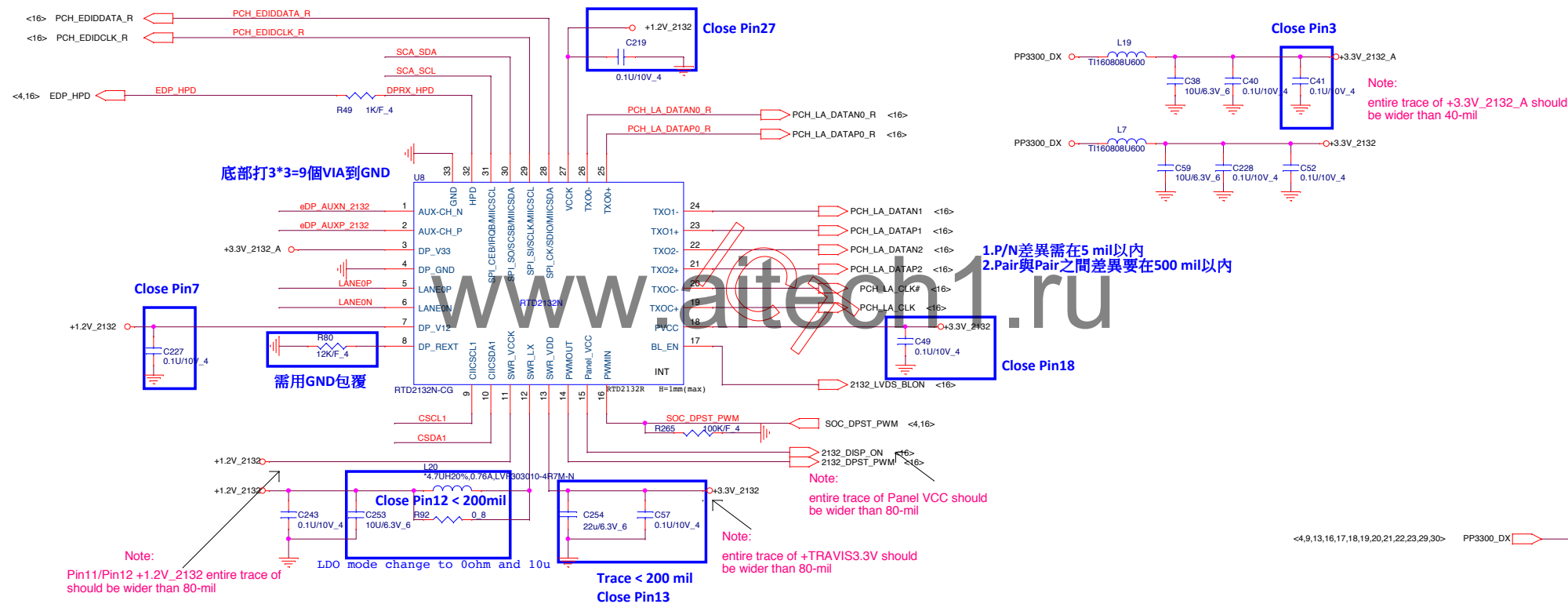
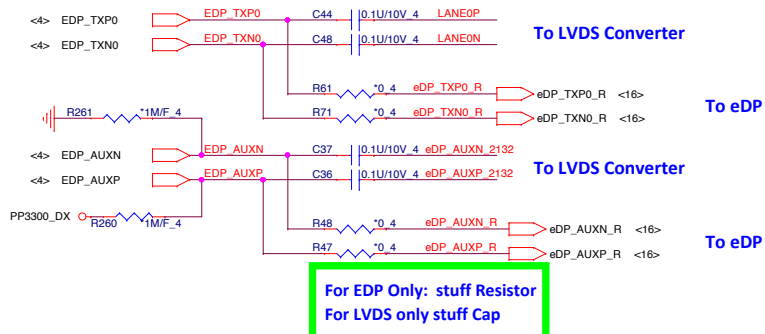
Combine +1.35VSFR,+1.35V
power rail for cost saving

<6,7,9,11,13,28,30> PP1800_PCH_S5
<19> +3V_LTE
<4,5,6,7,9,11,13,17,22,30> PP1800_PCH
<22,27,30> +3VSUS
<2,8,12,27,30> PP1350

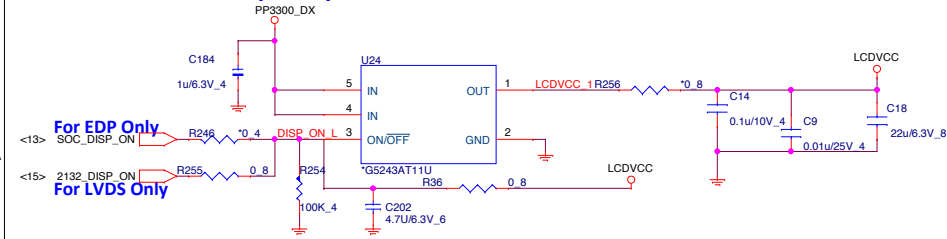


PROJECT : Y0A
Quanta Computer Inc.

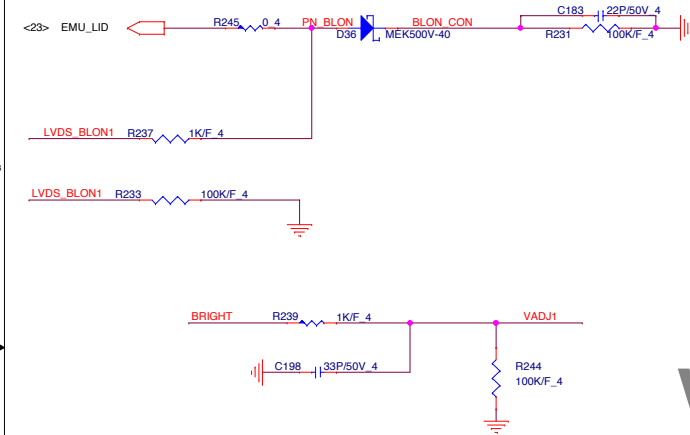
Size Custom	Document Number Level Shifter (SOC_DEV)	Rev 1A
Date: Monday, July 21, 2014	Sheet 14 of 31	



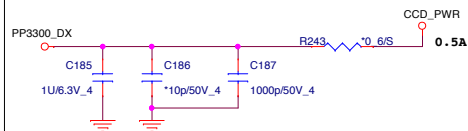
eDP /LVDS Power(VGA)



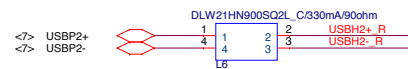
LID Switch



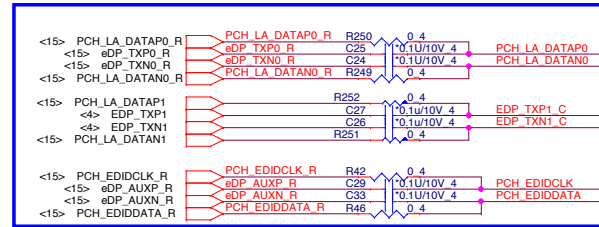
CCD power(CCD)



CCD USB(CCD)

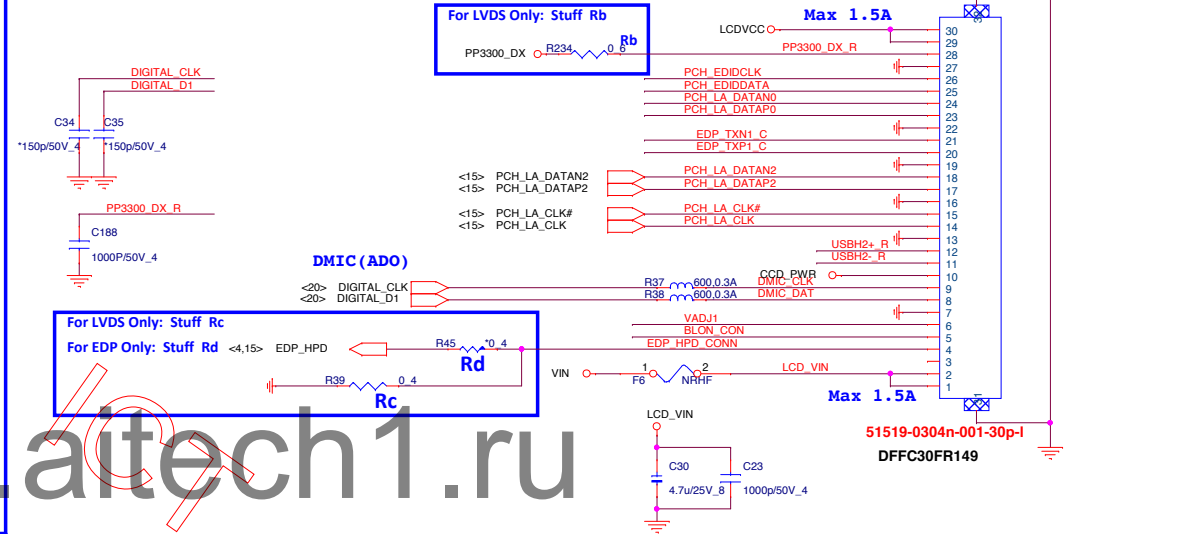


For LVDS Only stuff R, For EDP only stuff C

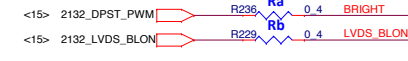


eDP/LVDS

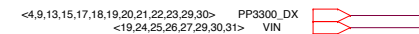
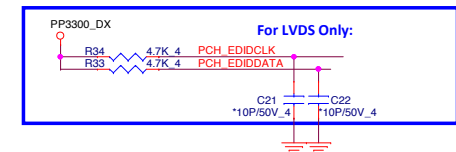
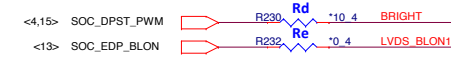
Need New LCD Cable



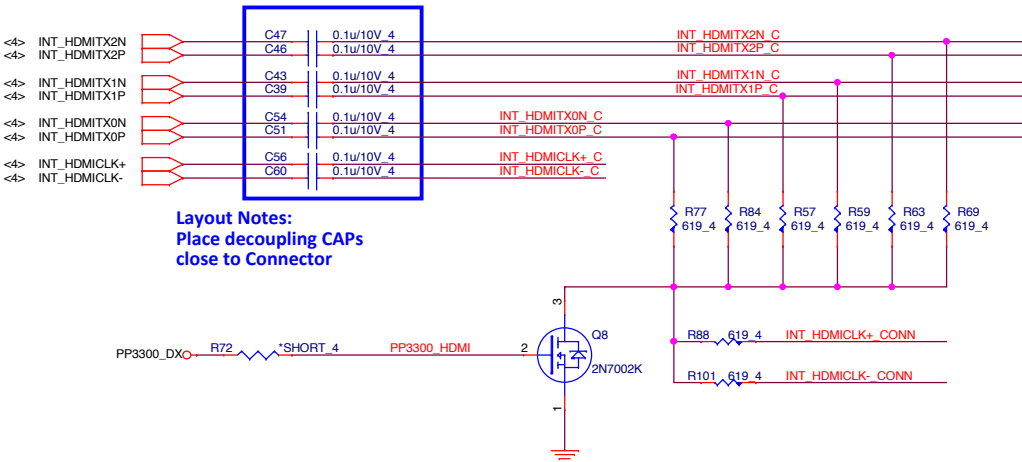
For LVDS Only: stuff Ra,Rb



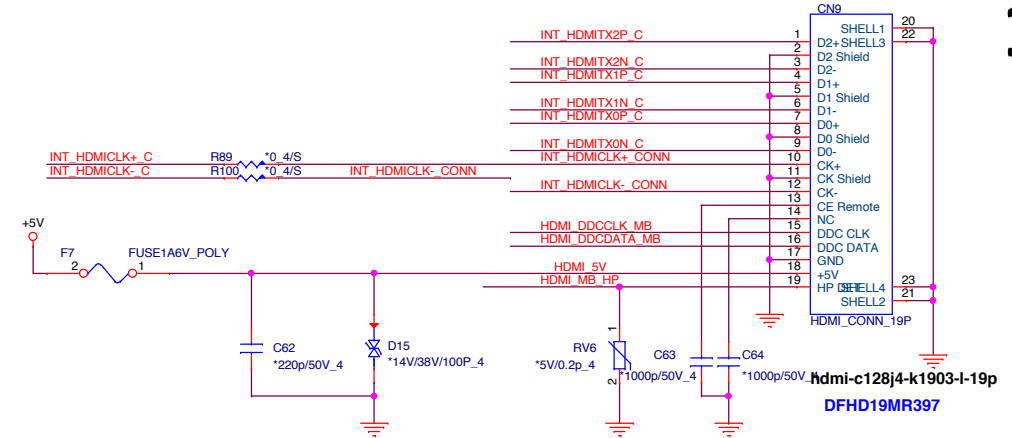
For EDP Only: stuff Rd,Re



HDMI Cost Reduced level shift (HDM)

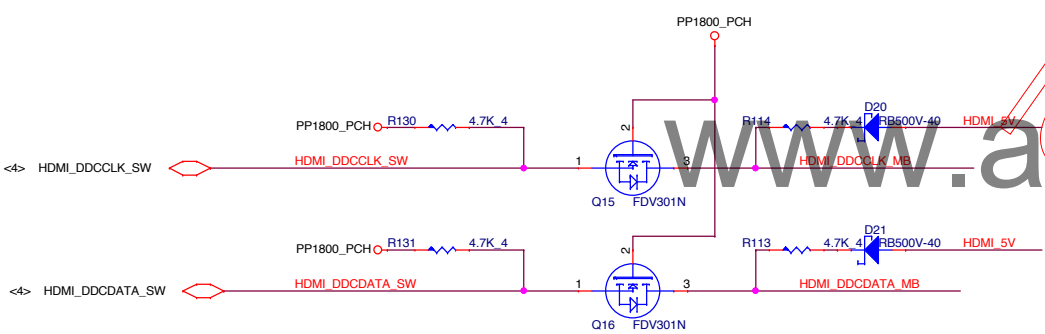


HDMI connector (HDM)

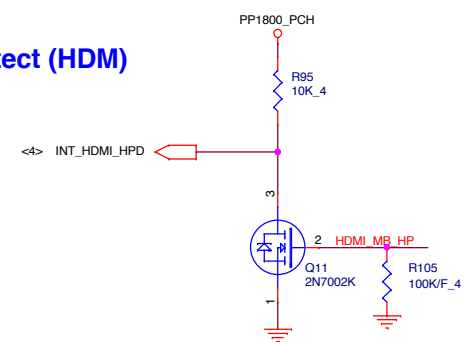


17

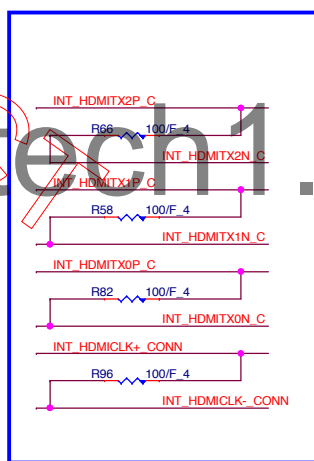
HDMI DDC (HDM)



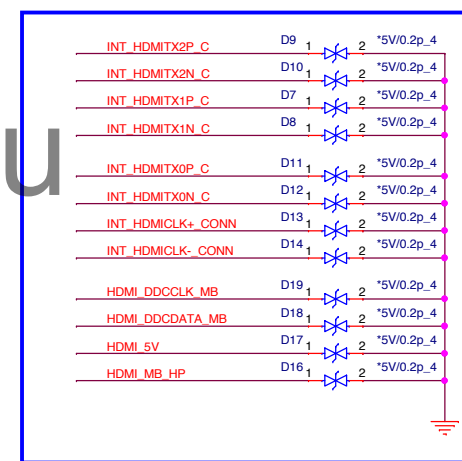
HDMI-detect (HDM)



EMI



ESD 靠近HDMI CONNECTOR(CN16)

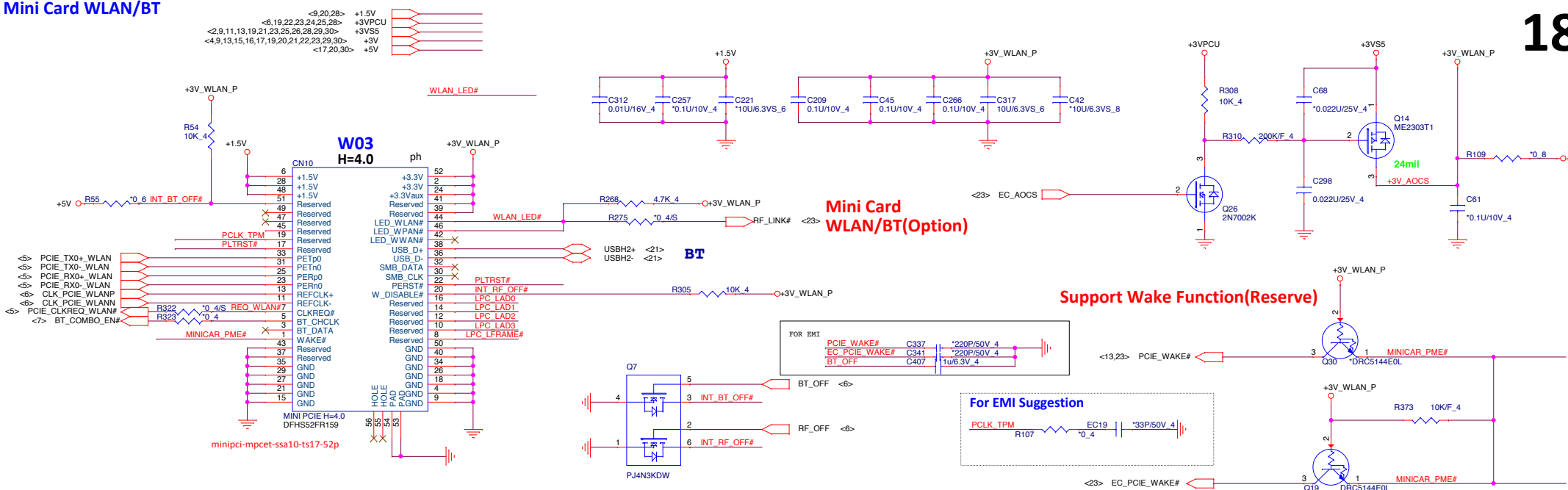


<4,9,13,15,16,18,19,20,21,22,23,29,30> PP3300_DX
<4,5,6,7,9,11,13,14,22,30> PP1800_PCH
<18,20,30> +5V

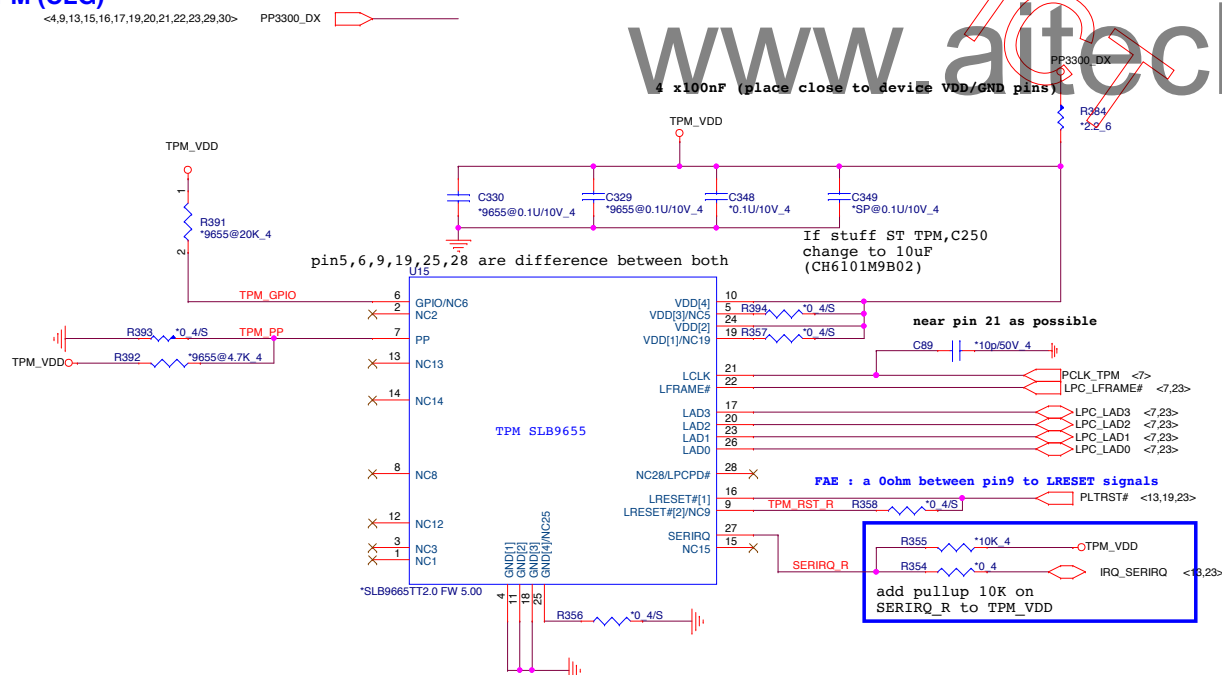
PROJECT : Y0A
Quanta Computer Inc.

Size Custom	Document Number HDMI	Rev 1A
Date: Monday, July 21, 2014		Sheet 17 of 31

Mini Card WLAN/BT

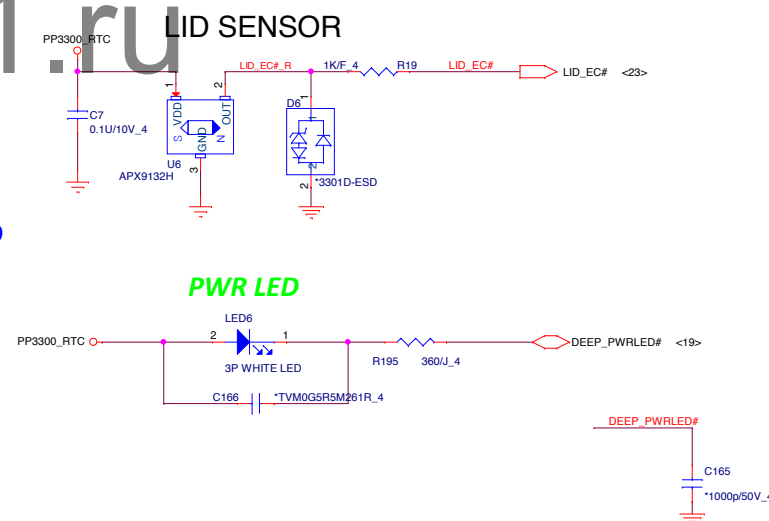


TPM (CLG)

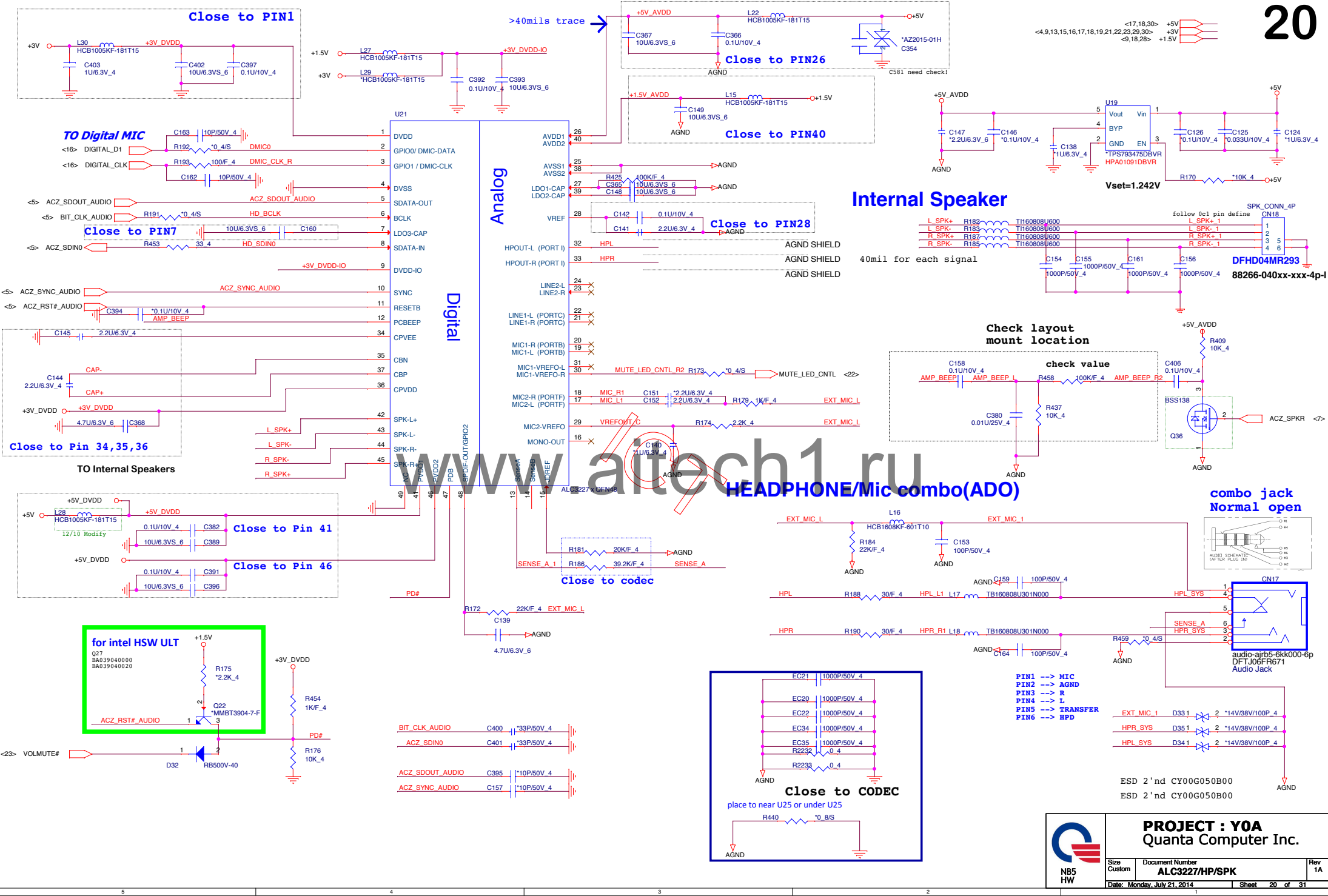


AL009665K01 IC OTHER(28P)SLB9665TT2.0 FW 5.00(TSSOP)

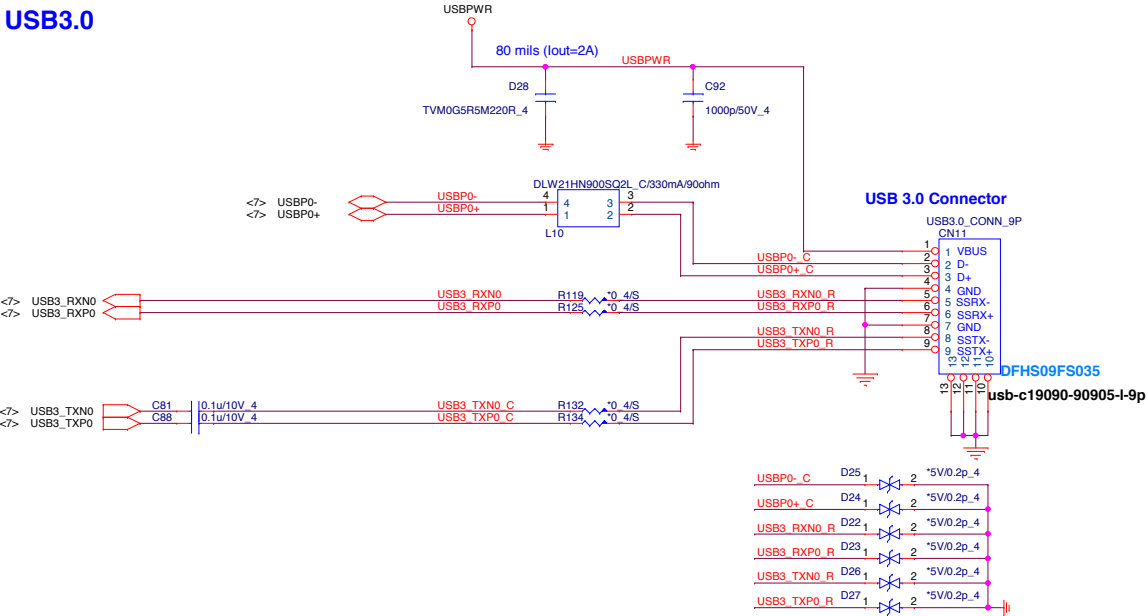
LID



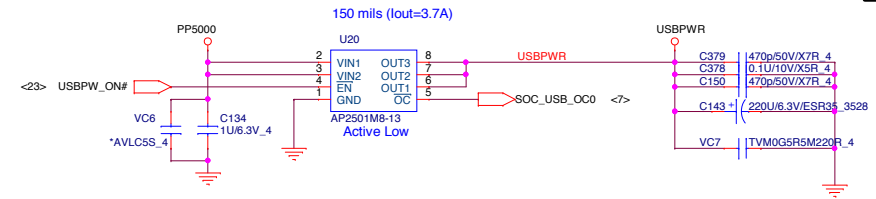
<6,19,22,23,24,25,28> PP3300_F



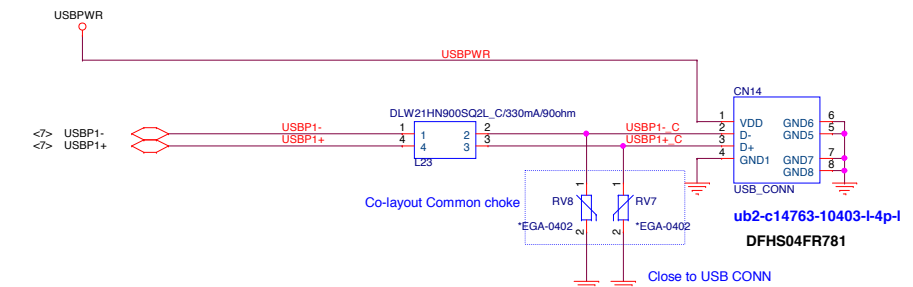
USB3.0



USB POWER SWITCH

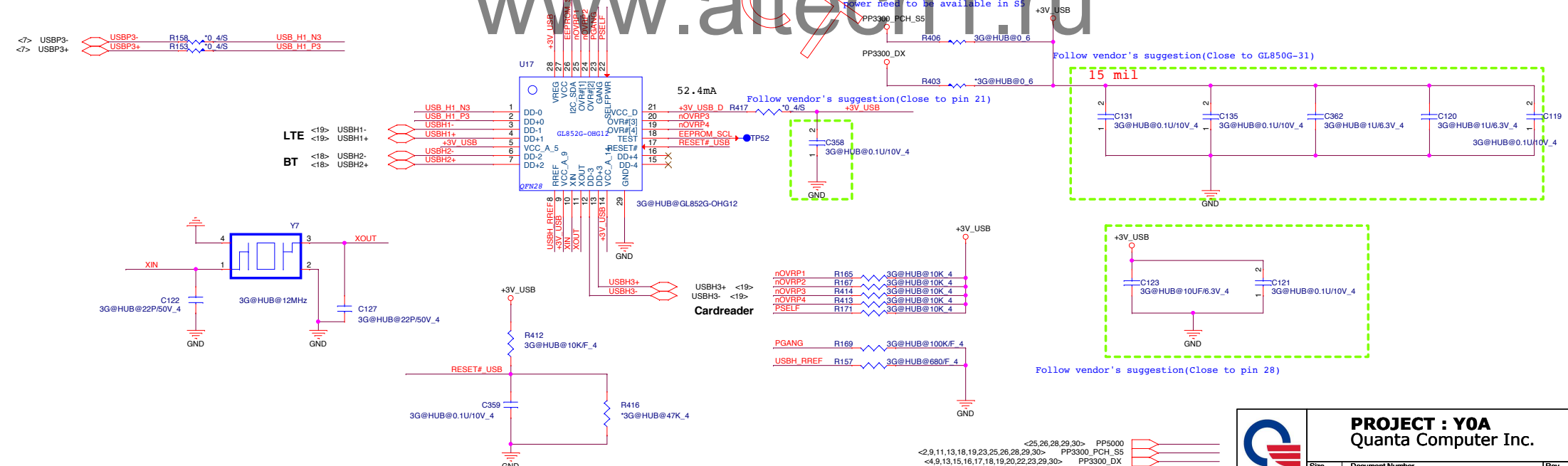


USB 2.0

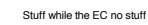


USB 2.0 HUB

3G@HUB@ means HUB will be stuffed if 3G/LTE exists

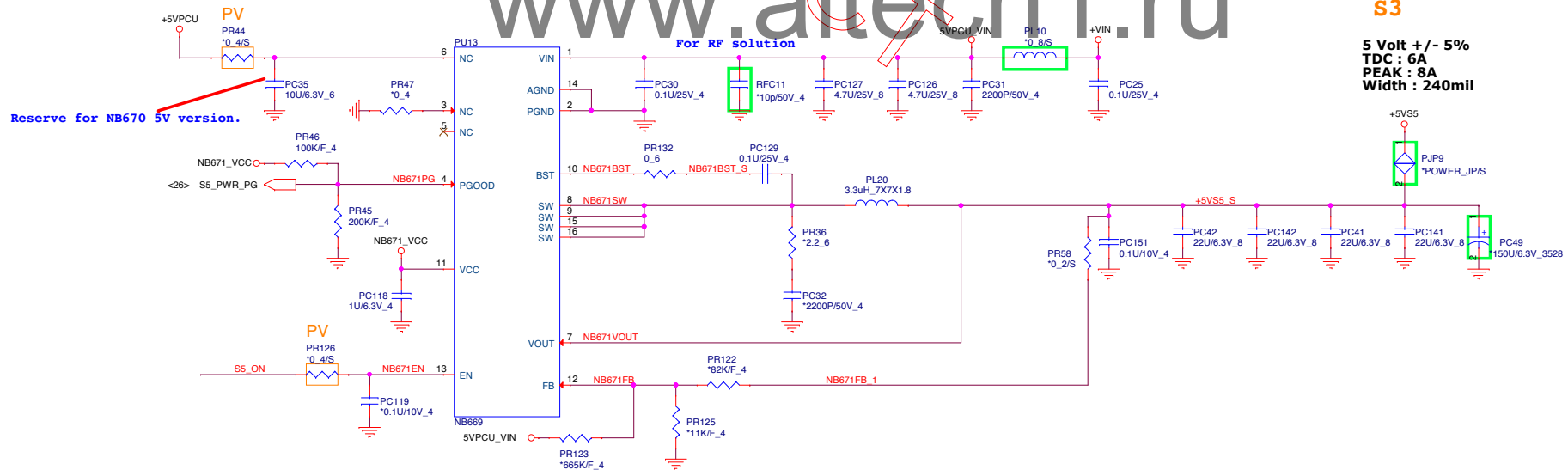
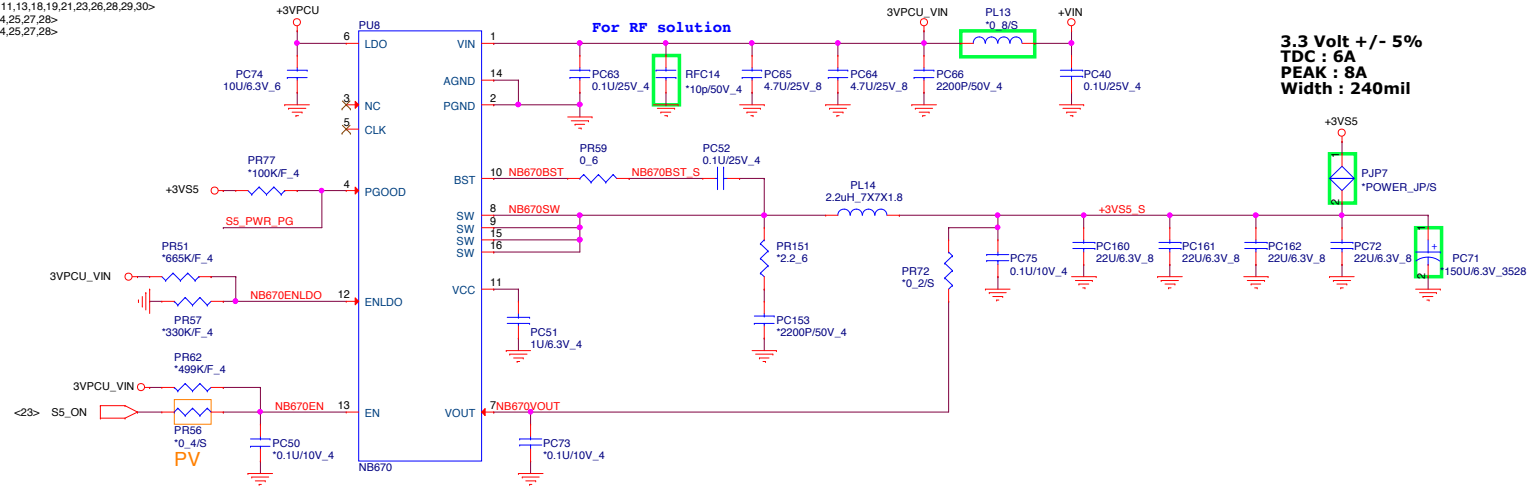


23



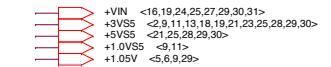
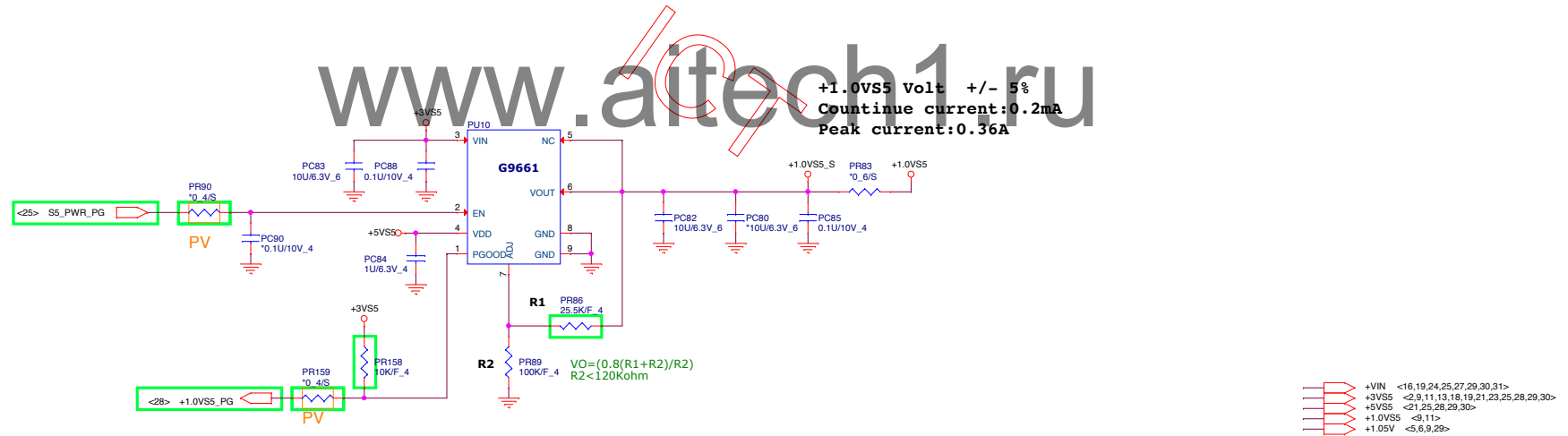
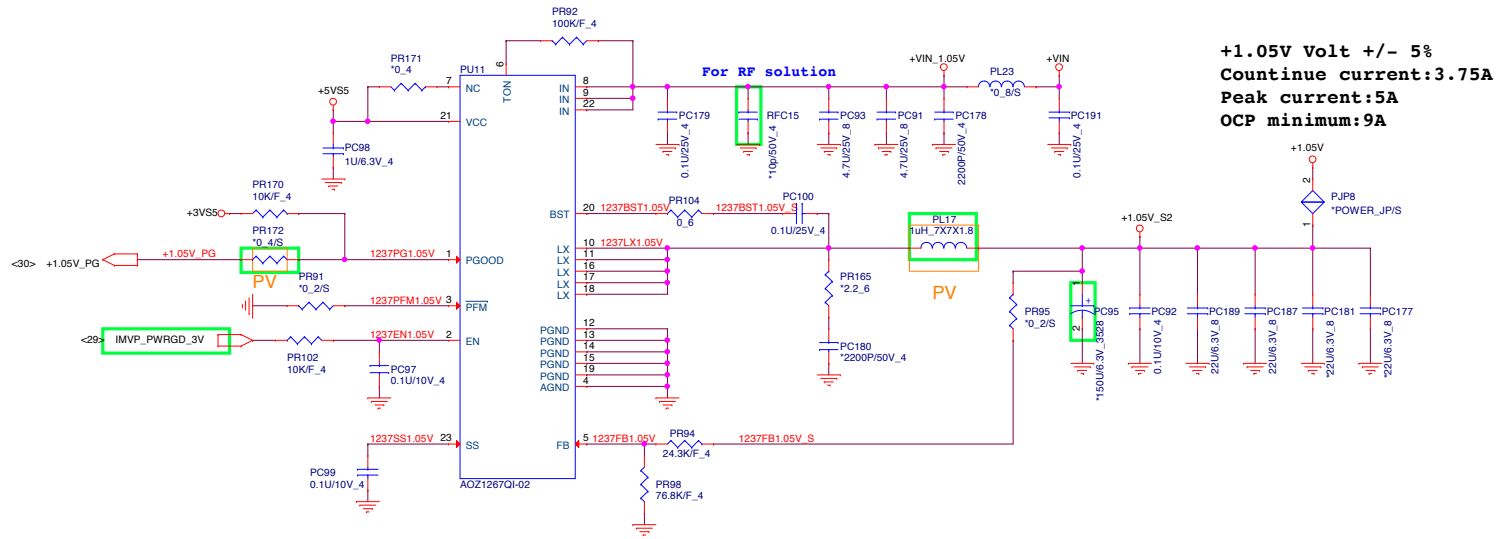
45/65W DC_JACK

+VIN <16,19,24,26,27,29,30,31>
 +3VPCU <6,18,19,22,23,24,28>
 +3VS5 <2,9,11,13,18,19,21,23,26,28,29,30>
 +5VPCU <24,25,27,28>
 +5VS5 <24,25,27,28>




PROJECT : Y0A
Quanta Computer Inc.

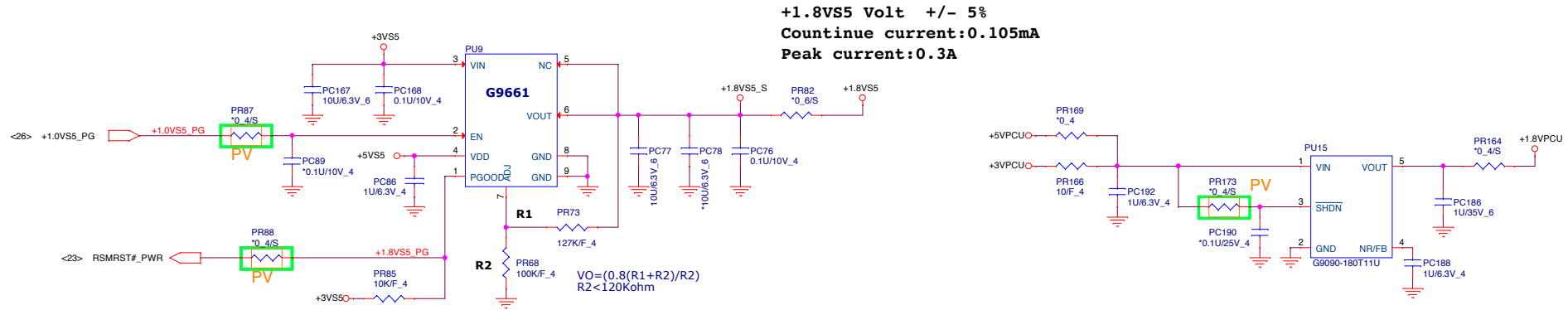
Size Custom	Document Number 3V/5V (NB670/NB669)	Rev 1A
Date: Monday, July 21, 2014	Sheet 25 of 31	



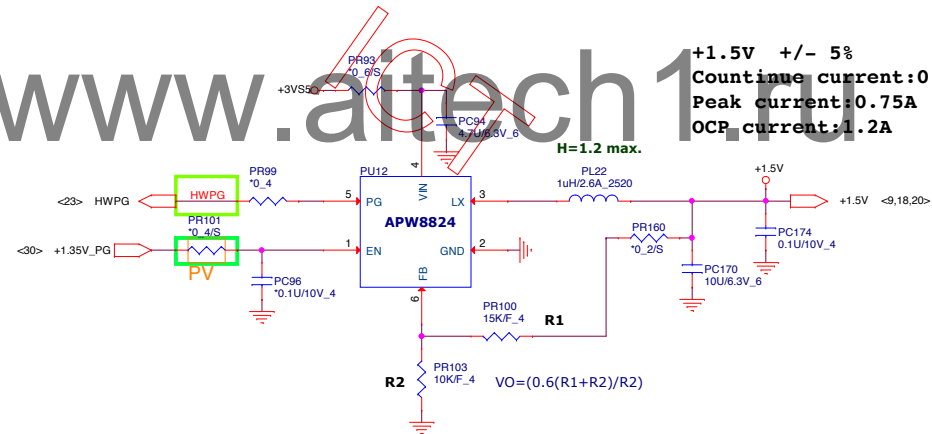


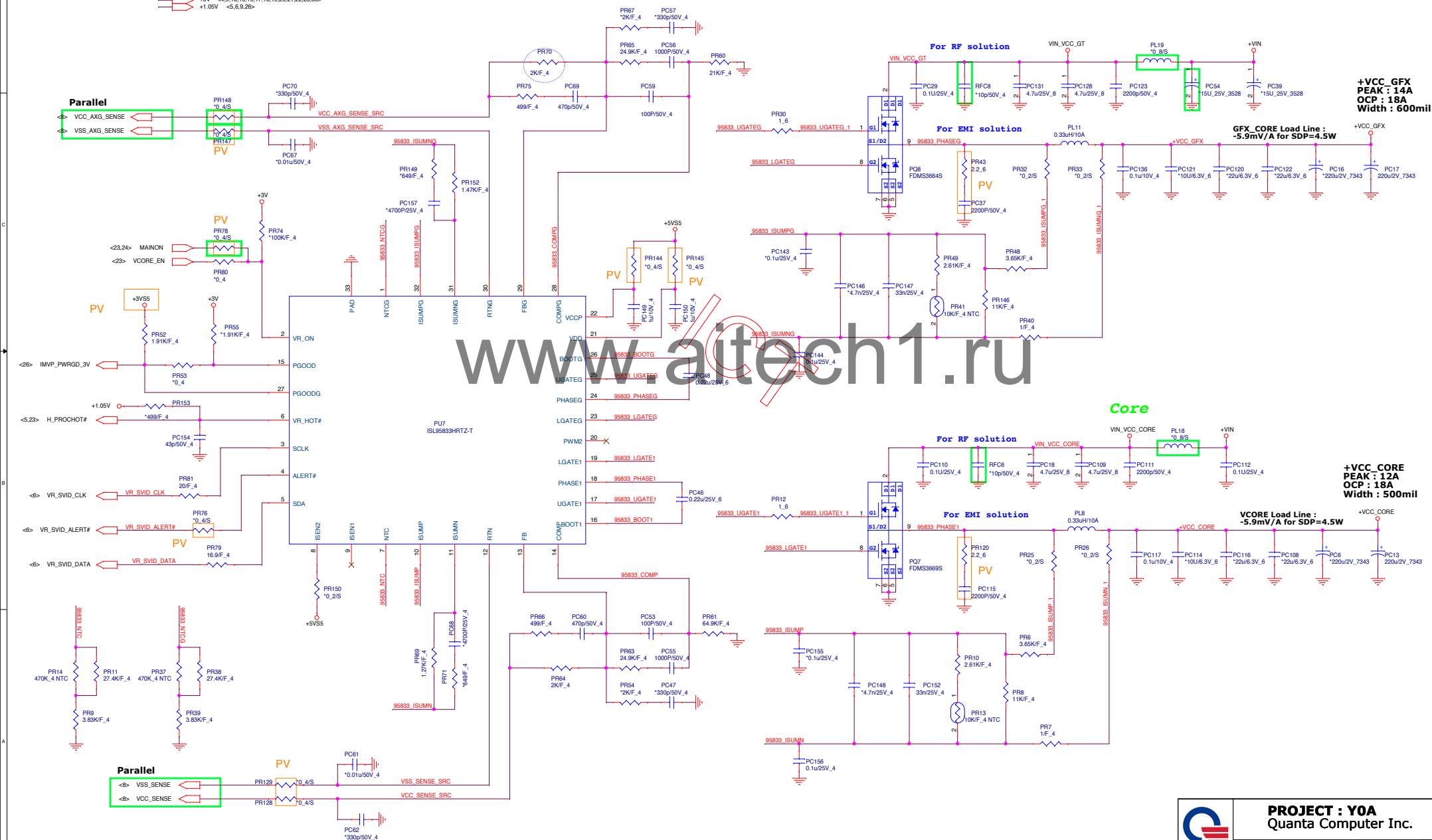
 NB5 HW	PROJECT : Y0A Quanta Computer Inc.		
	Size Custom	Document Number DDR 1.35V(UP1740S)	Rev 1A
	Date: Monday, July 21, 2014	Sheet	27 of 31

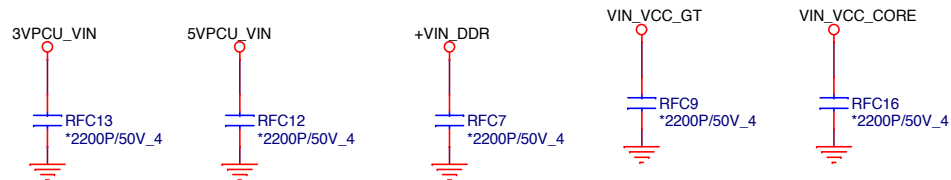
+1.8VS5 <6,7,9,11,13,14,30>
 +5VS5 <21,25,26,29,30>
 +3VS5 <2,9,11,13,18,19,21,23,25,26,29,30>
 +3VPCU <6,18,19,22,23,24,25>
 +5VPCU <24,25,27>
 +1.8VPCU <23>



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 +1.5V +/- 5%
 Countinue current:0.3A
 Peak current:0.75A
 OCP current:1.2A



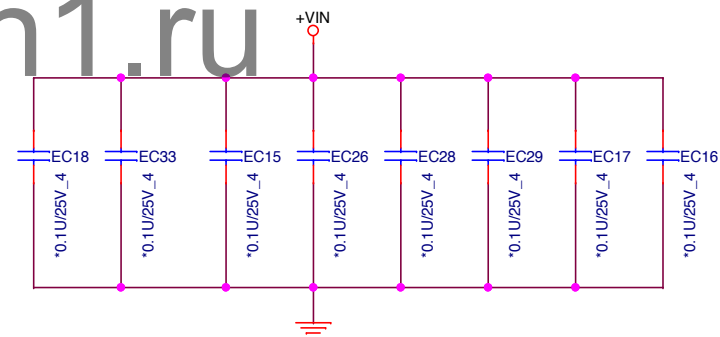
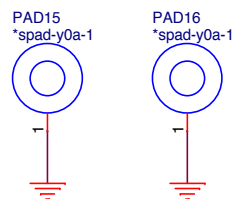
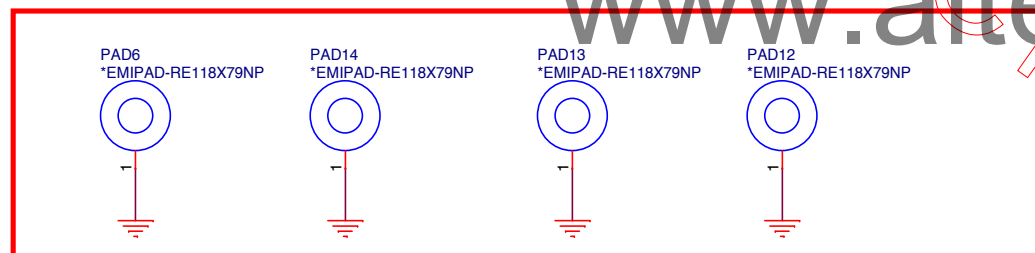




TOP



BOTTOM

NB5
HW

PROJECT : Y0A
Quanta Computer Inc.

Size
CustomDocument Number
EM/RFRev
1A

Date: Monday, July 21, 2014

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